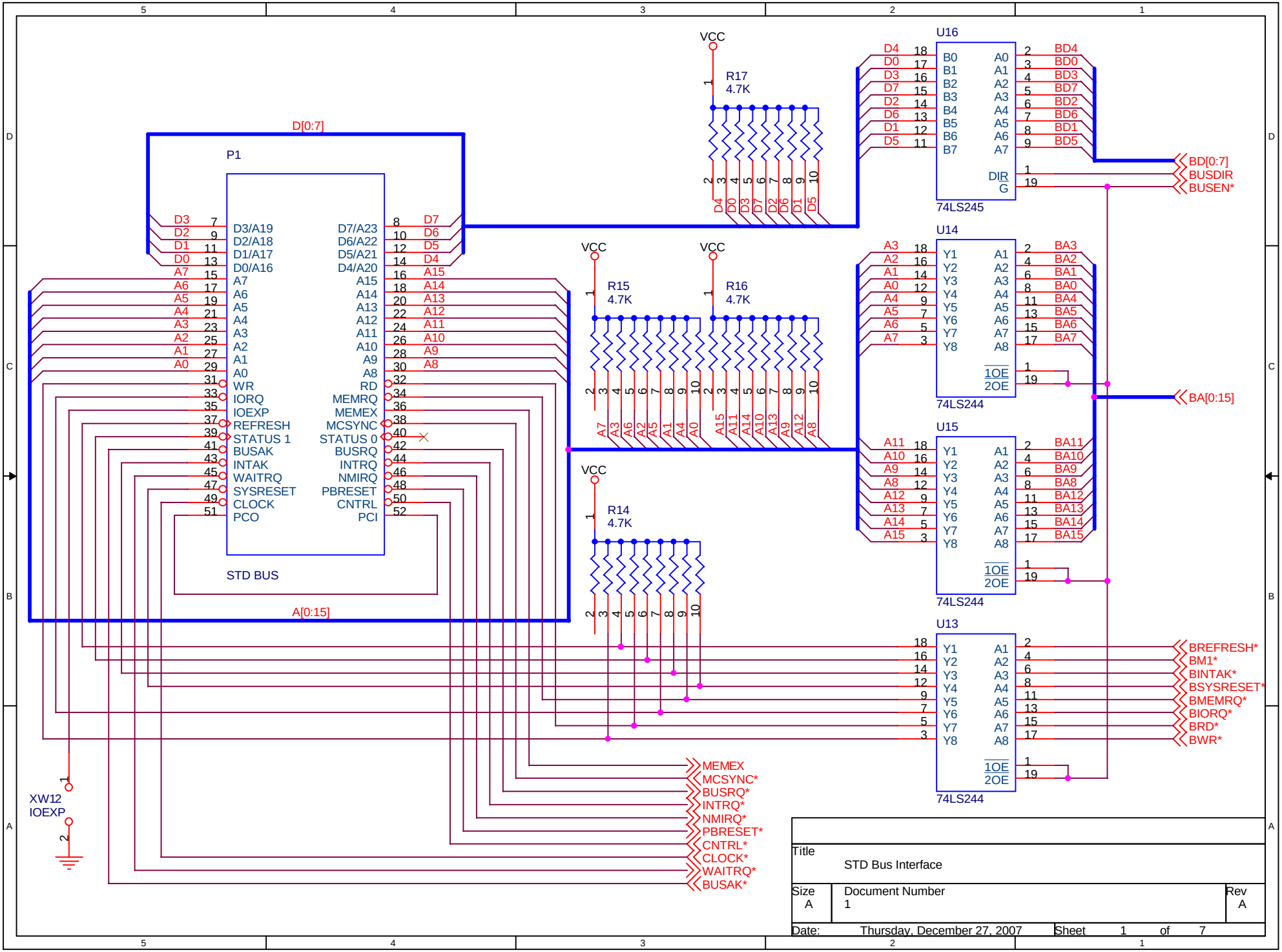
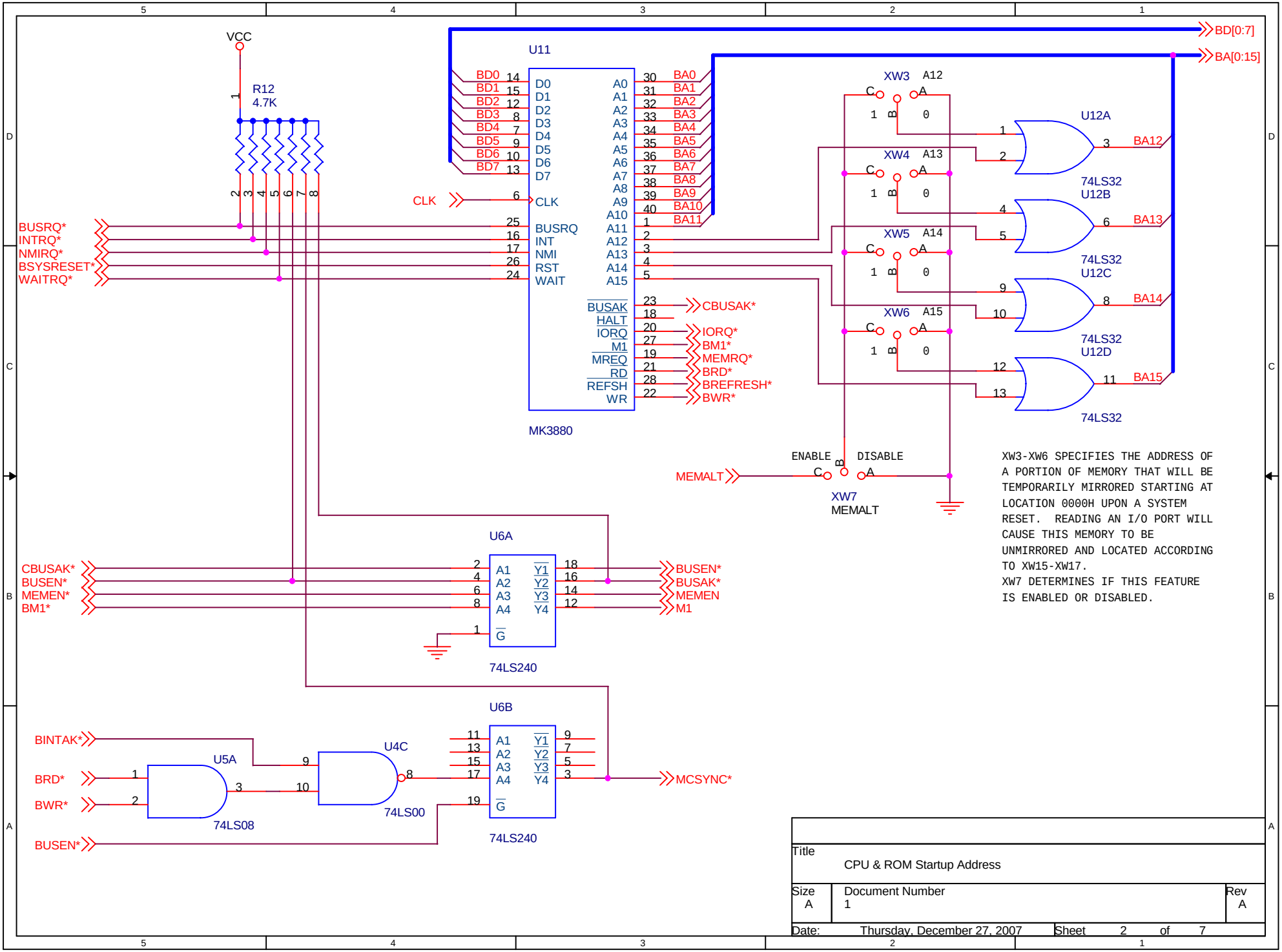


Title	STD Bus Interface	
Size A	Document Number 1	Rev A
Date: Thursday, December 27, 2007	Sheet 1 of 7	



Title	CPU & ROM Startup Address	
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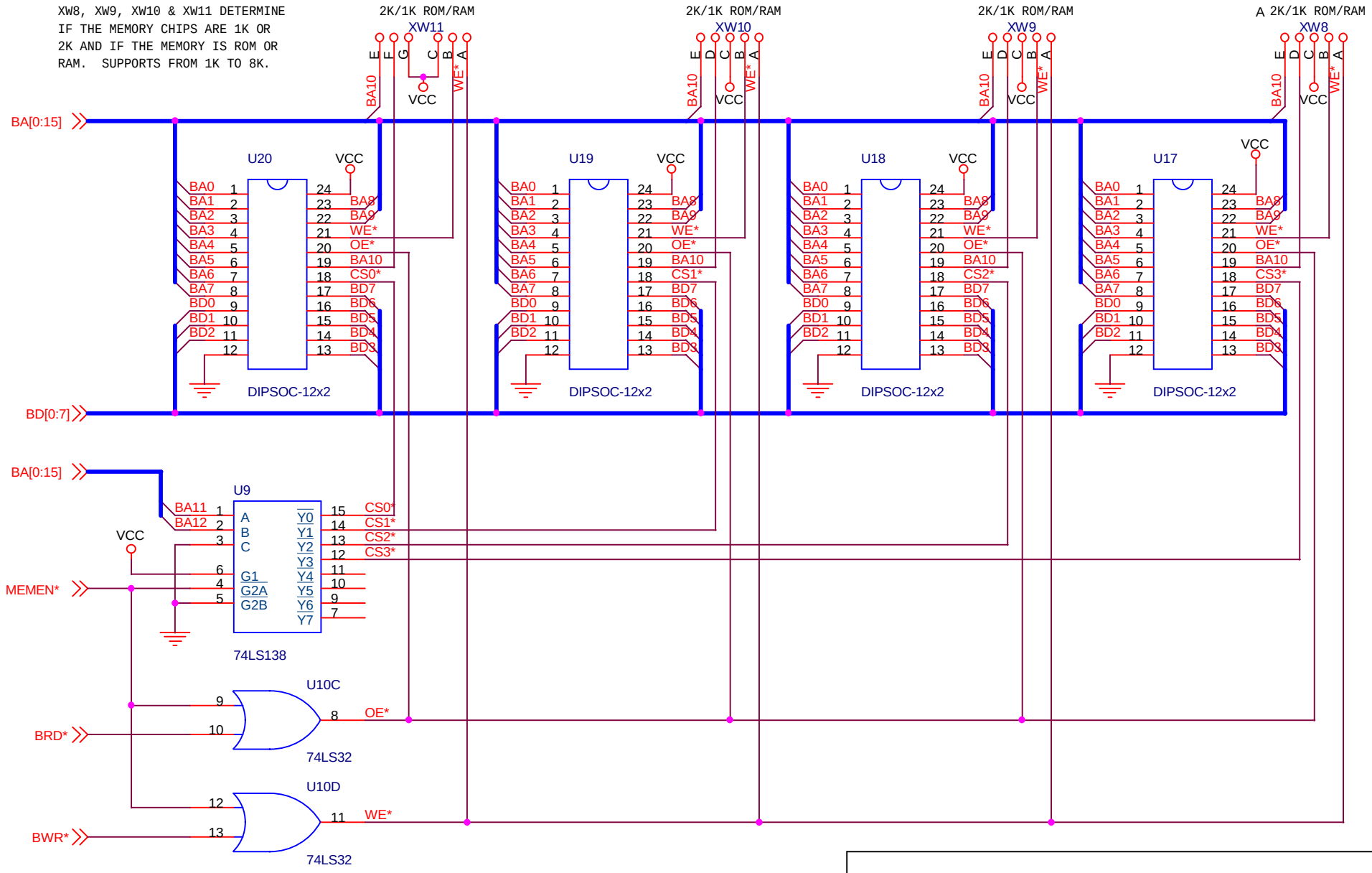
XW8, XW9, XW10 & XW11 DETERMINE
IF THE MEMORY CHIPS ARE 1K OR
2K AND IF THE MEMORY IS ROM OR
RAM. SUPPORTS FROM 1K TO 8K.



Title	Local Memory	
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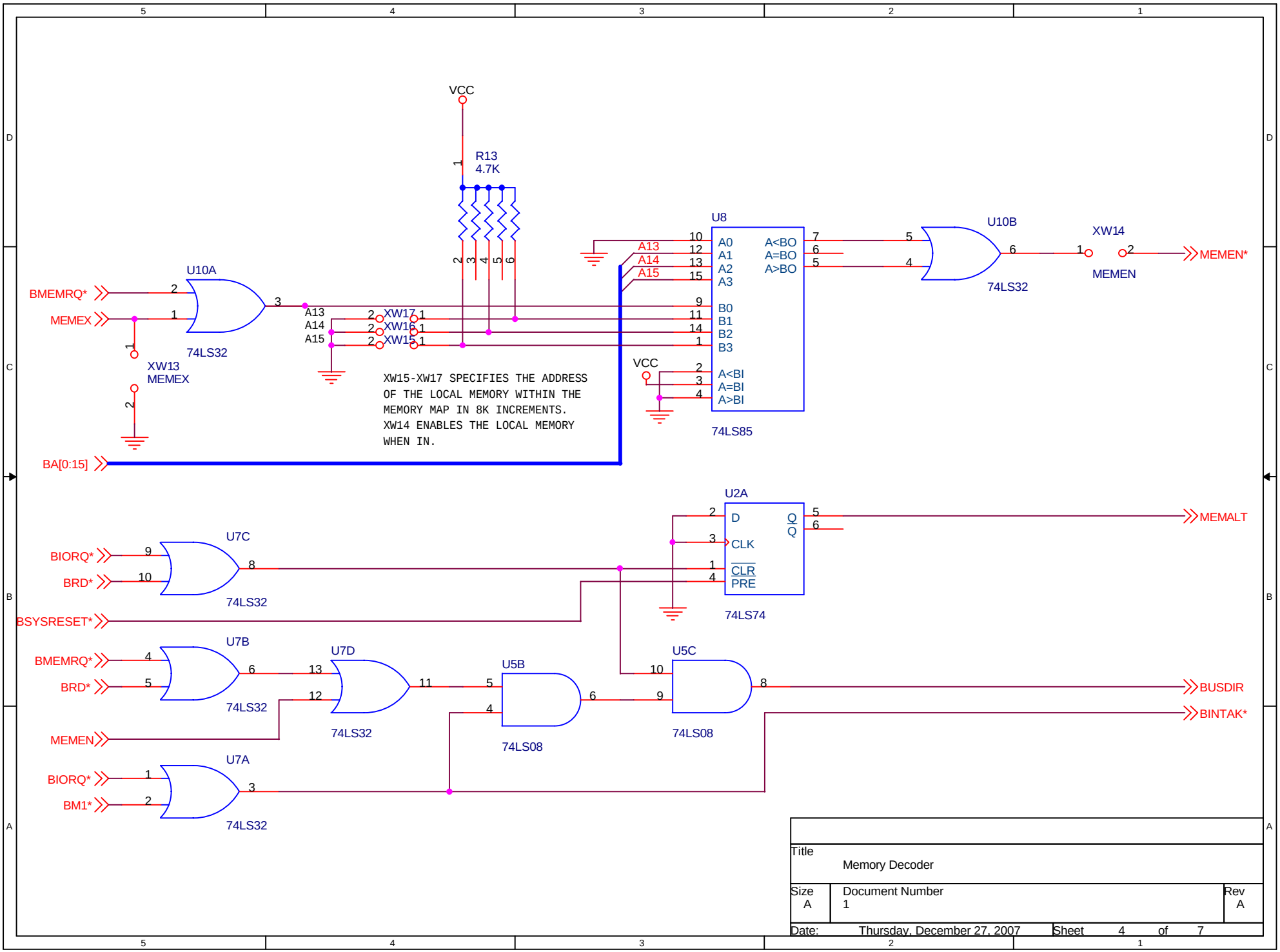
Date: Thursday, December 27, 2007	Sheet 3 of 7	



Title	Memory Decoder	
Size A	Document Number 1	Rev A
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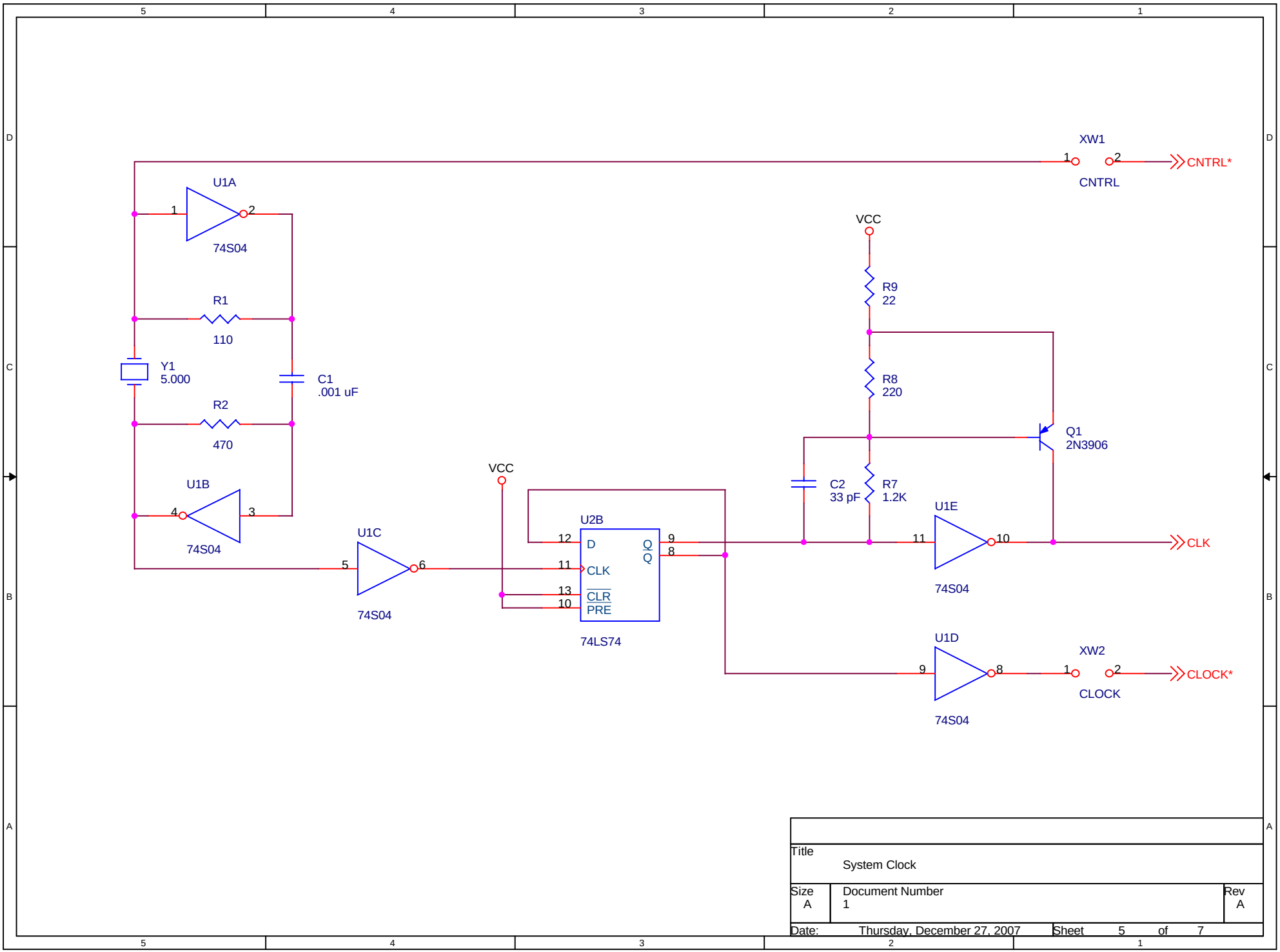
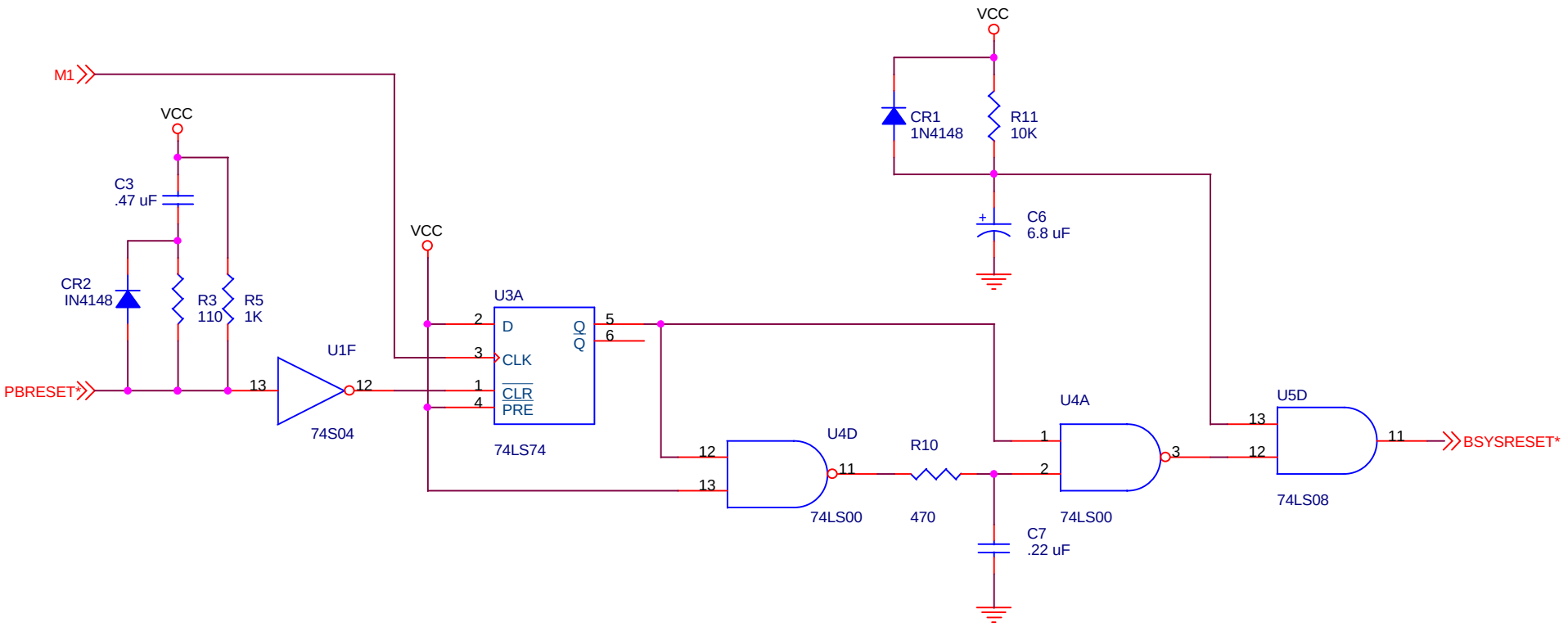
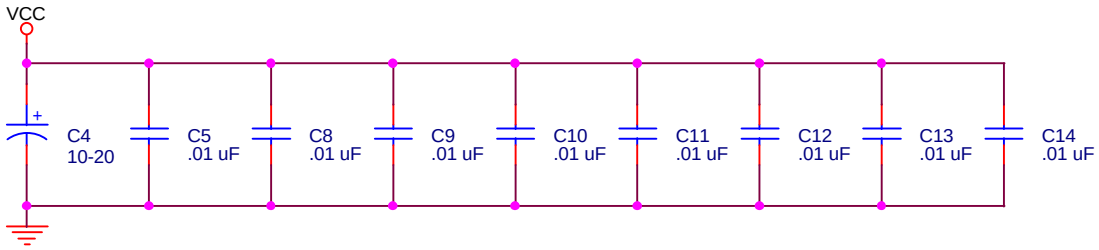
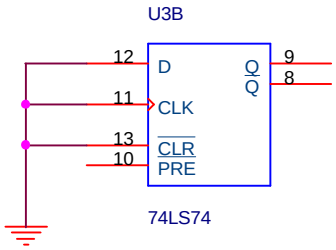


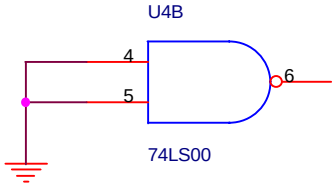
Title: System Clock		
Size: A	Document Number: 1	Rev: A
Date: Thursday, December 27, 2007	Sheet 5 of 7	



Title: Reset Circuit		
Size: A	Document Number: 1	Rev: A
Date: Thursday, December 27, 2007	Sheet 6 of 7	







Title: Miscellaneous		
Size: A	Document Number: 1	Rev: A
Date: Thursday, December 27, 2007	Sheet 7 of 7	